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U.S. PATENT & TRADEMARK OFFICE

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IN THE UNITED STATES
PATENT AND TRADEMARK OFFICEJC997 U.S. PTO
09/00/2001
03/06/01

PATENT APPLICATION

By Express Mail - Label No. EK 976 499 625 US

INVENTOR John Howard Coleman,
40 Overlook Road, Locust Valley, NY 11560**TITLE** Semiconductor Fabrication and Structure for
Field-Effect and Bipolar Transistor DevicesASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231**SIR:**

Enclosed are the following papers relating to the above-named application for patent:

15 pages of Specification with Claims and Abstract
3 sheets of Drawing(s)
2 pages of Declaration and Power of Attorney

CLAIMS AS FILED

	FILED	EXTRA	RATE	CALCULATIONS
Total Claims	21 - 20 =	1	x \$ 9 =	\$ 9
Independent Claims	3 - 3 =	0	x \$ 40 =	
Basic Fee				<u>\$355</u>
			TOTAL	<u>\$364</u>

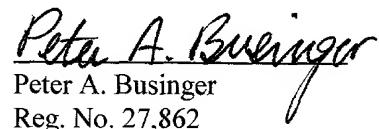
The applicant qualifies for Small-Entity status.

The applicant claims priority based on his provisional application No. 60/187,290 filed March 6, 2000, entitled Method of Making New Semiconductor Material for Field Effect Transistors by Hydrogen Ion Implantation.

Please file the application and charge the applicant's **Deposit Account No. 16-2080** the amount of \$364.00, to cover the filing fee. A copy of this letter is enclosed. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or credit **Deposit Account No. 16-2080** as required to correct the error.

Please address all correspondence to **Peter A. Businger, Esq., 344 Valleyscent Avenue, Scotch Plains, NJ 07076-1170**, telephone (908) 322-4835.

Respectfully,


Peter A. Businger
Reg. No. 27,862
Attorney for Applicant(s)Date: March 6, 2001